

#3
6-12-02
Mellish

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Craig T. Salling, et al.

Art Unit: TBD

Serial No.: 10/051,962

Examiner: TBD

Filed: 01/16/02

Docket: TI-32535

For: Eliminating Substrate Noise by an Electrically Isolated High-Voltage I/O Transistor

LETTER TO THE OFFICIAL DRAFTSPERSON



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|--|-----------------------|
| MAILING CERTIFICATE UNDER 37 C.F.R. §1.8(A) | |
| I hereby certify that the above correspondence is being deposited with the U.S. Postal Service on <u>4-5-02</u> as First Class Mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231. | |
| <u>Karen Vertz</u> Karen Vertz | <u>4-5-02</u> Date |

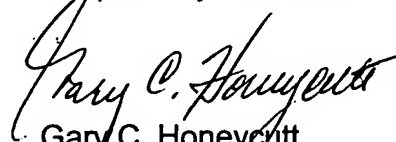
Assistant Commissioner for
Patents
Washington, D. C. 20231

Dear Sir:

Enclosed are **FOUR (4)** sheets of formal drawings for the above-referenced case. Please charge any necessary fees to Deposit Account No. 20-0668 of Texas Instruments Incorporated. This sheet is enclosed in triplicate.

Texas Instruments Incorporated
P.O. Box 655474 M/S 3999
Dallas, Texas 75265
(214) 939-8651

Respectfully submitted,


Gary C. Honeycutt
Reg. No. 20,250
Attorney for Applicants